Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.

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**Top Material: Al**

 **Backside Material: Si**

**Bond Pad Size: .004” X .004”**

**Backside Potential:**

**Mask Ref:**

**APPROVED BY: DK DIE SIZE .046” X .078” DATE: 11/9/21**

**MFG: THOMSON THICKNESS .015” P/N: LF156**

**DG 10.1.2**

#### Rev B, 7/1